



**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

Confirmation No.: 7046

KOMURA et al.

Group Art Unit: 2816

Application No.: 09/921,561

Examiner: Minh T. Nguyen

Filed: August 6, 2001

Attorney Dkt. No.: 024016-00012

For: DELAY CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
CONTAINING A DELAY CIRCUIT AND DELAY METHOD

**RESPONSE UNDER 37 C.F.R. §1.111**

**Mail Stop Amendment**

Director for the U.S. PTO

P.O. Box 1450

Alexandria, VA 22313-1450

Date: October 29, 2004

Sir:

This paper is filed in response to the Office Action dated July 12, 2004, the period for response having been extended one-month from October 12, 2004, to November 12, 2004 by the enclosed Petition for Extension of Time. In the prosecution of the application identified above, please consider the following: